

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE  
WASHINGTON, D.C. 20231SA  
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Inventor: Subhash Narang et al.

Examiner: Not yet determined

Serial No: US national phase of  
PCT/US98/25088

Art Unit: Not yet determined

Filed: 24 November 1998

For: Printing of Electronic Circuits &  
ComponentsPRELIMINARY AMENDMENT FILED CONCURRENTLY WITH  
US NATIONAL PHASE APPLICATIONThe Honorable Commissioner  
of Patents and Trademarks  
Washington, D.C. 20231

Dear Sir:

Please enter the following as a preliminary amendment.

IN THE CLAIMS

18. (Amended) The method of claim 1 [any of claims 1 -16] wherein the first solution applied to the surface contains no more than 2% particulates by weight.
19. (Amended) A method of printing an electronic circuit comprising:  
printing a plurality of components according to one of the methods of claim 1 [any of claims 1 -16]; and  
applying the first solution to the surface in a pattern that connects at least two of the plurality of components, and initiating a redox reaction in the pattern to produce a conductive trace between the at least two components.